## AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

## Listing of Claims:

Claims 1-4 (cancelled)

5. (currently amended) The serial communication device according to claim 1,

A serial communication device, comprising:

a serial interface to receive data; and

a direct memory access controller to transfer said data received by said serial interface from said serial interface to a first memory external to the direct memory access controller,

wherein said direct memory access controller is started up before said serial interface receives said data,

said direct memory access controller sets a number

larger than the number of data received at a time by the

serial interface as a number of transfers to said first

memory before said serial interface receives said data,

when the number of data transferred from said serial interface to said first memory reaches said number set as the number of transfers, said direct memory access

controller outputs a direct memory access transfer end interrupt signal to a central processing unit,

wherein said first memory is comprised of two or more  $\frac{1}{1}$  memory areas,  $\frac{1}{1}$ 

said direct memory access controller has a continuous transfer function and transfers said data from said serial interface to said first memory while alternately switching the destinations of the data received by said serial interface among said two or more memory areas, as triggered by said direct memory access transfer end interrupt signal or a receive timeout interrupt signal, and

said data transferred to said first memory is

transferred to an application/driver work area, as triggered

by said direct memory access transfer end interrupt signal

or said receive timeout interrupt signal, for use by an

application or a driver.

6. (New) The serial communication device according to claim 5, wherein the transfer of data to said application/driver work area includes direct memory access of a channel different from a direct memory access channel used in the transfer of said data from said serial interface to said first memory.